FORWARD ERROR CORRECTION SCHEME COMPATIBLE WITH THE BIT ERROR SPREADING OF A SCRAMBLER

Abstract

A method for combining a simple forward error correction code i.e., a Hamming-like code with scrambling and descrambling functions is disclosed. Therefore, irrespective of the information to be transported, received data may be corrected, bit error spreading effects being handled, while providing desirable signal characteristics such as signal DC balance and enough signal transitions. The overhead introduced by the method is a modest increase over the original overhead of the 10Gb Ethernet 64B/66B code.